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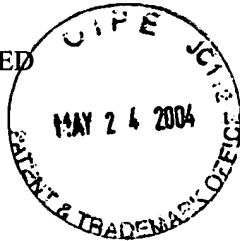
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23494 7590 03/31/2004

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Ann Trent	(Depositor's name)
<i>Ann Trent</i>	(Signature)
5-17-04	(Date)

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
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10/068,014

02/05/2002

Keith A. Joyner

TI-29912

7239

TITLE OF INVENTION: METHOD FOR MANUFACTURING AND STRUCTURE FOR TRANSISTORS WITH REDUCED GATE TO CONTACT SPACING

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1330	\$300	\$1630	06/30/2004

EXAMINER	ART UNIT	CLASS-SUBCLASS
BREWSTER, WILLIAM M	2823	438-184000

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1 Peter K. McLarty
2 W. James Brady III
3 Frederick J. Telecky Jr.

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Texas Instruments Incorporated

Dallas, Texas

Please check the appropriate assignee category or categories (will not be printed on the patent); ☐ individual ☒ corporation or other private group entity ☐ government

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(Date)

Peter K. McLarty, Reg. No. 44,923 5/19/2004

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05/26/2004 WASFAM2 00000065 200668 10068014

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